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a. Serial No.	f. Foreign Priority	k. Print Claim(s)	<b>p. PTO-1449</b>
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

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- b. Text Continuity
- c. Holes through Data
- d. Other Missing Text
- e. Illegible Text
- f. Duplicate Text
- g. Brief Description
- h. Sequence Listing
- i. Appendix
- j. Amendments
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<p>Substitute for form 1449B/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p>Date Submitted: June 13, 2002</p> <p>Use as many sheets as necessary)</p> <p>1 of 7</p>	<p align="center"><b>Complete if Known</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td>Application Number</td> <td>10/066,376</td> </tr> <tr> <td>Filing Date</td> <td>02/5/2002</td> </tr> <tr> <td>First Named Inventor</td> <td>Igor KOUZNETSOV et al.</td> </tr> <tr> <td>Group Art Unit</td> <td>2821</td> </tr> <tr> <td>Examiner Name</td> <td>Unassigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>035905-0118</td> </tr> </table>	Application Number	10/066,376	Filing Date	02/5/2002	First Named Inventor	Igor KOUZNETSOV et al.	Group Art Unit	2821	Examiner Name	Unassigned	Attorney Docket Number	035905-0118
Application Number	10/066,376												
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First Named Inventor	Igor KOUZNETSOV et al.												
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Attorney Docket Number	035905-0118												

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	B1	4,500,905		Shibata		
	B2	6,185,122		Johnson et al.		
	B3	3,414,892		McCormack et al.	12/13/1968	
	B4	3,432,827		Sarno	3/11/1969	
	B5	4,535,424		Reid		
	B6	4,630,096		Drye		
	B7	4,672,577		Hirose		
	B8	4,710,798		Marcantonio		
	B9	4,811,082		Jacobs		
	B10	5,001,539		Inoue et al.		
	B11	5,089,862		Warner, Jr. et al.		
	B12	5,160,987		Pricer et al.		
	B13	5,191,405		Tomita et al.		
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	B15	5,266,912		Kledzik		
	B16	5,283,468		Kondo et al.		
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	B18	5,422,435		Takiar et al.		
	B19	5,426,566		Beilstein, Jr.		
	B20	5,434,745		Shokrgozar et al.		
	B21	5,453,952		Okudaira		
	B22	5,455,455		Kurtz et al.		
	B23	5,468,997		Imai et al.		
	B24	5,471,090		Deutsch		
	B25	5,481,133		Hsu		
	B26	5,495,398		Takiar et al.		
	B27	5,502,289		Takiar et al.		
	B28	5,523,622		Harada et al.		
	B29	5,523,628		Williams et al.		
	B30	5,552,963		Burns		
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	B34	6,612,570		Eide et al.		
	B35	5,654,220		Leedy		
	B36	5,693,552		Hsu		
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	B42	5,915,167		Leedy		

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SUBSTITUTE for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> Date Submitted: June 13, 2002 (Use as many sheets as necessary)		<b>Complete if Known</b>	
Application Number		10/066,376	
Filing Date		02/5/2002	
First Named Inventor		Igor KOUZNETSOV et al.	
Group Art Unit		2821	
Examiner Name		Unassigned	
Attorney Docket Number		035905-0118	

## U.S. PATENT DOCUMENTS

	U.S. Patent Document		
B43	5,969,380		Syyedy
B44	5,976,953		Zavracky et al.
B45	5,985,693		Leedy
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B56	6,337,521	B1	Masuda
B57	6,353,265	B1	Michii
B58	6,355,501	B1	Fung et al.
B59	6,197,641	B1	Hergenrother et al.

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	B60	EPO	0 073 486	A2	Toyama et al.	8-26-1982		
	B61	JP	61-222216		Yohehara	10-2-1986		
	B62	WO	94/26083		Carson et al.	11-10-1994		
	B63	EPO	0 516 866	A1	Bayer et al.	12-9-1992		
	B64	EPO	0 644 548	A2	Bertin	9-2-1994		
	B65	EPO	0 800 137	A1	Genduso et al.	3-14-1997		
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	B70	EPO	0 387 834	A2	Wada	9-14-1990		

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	<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		<b>Application Number</b>	10/066,376
	Date Submitted: June 13, 2002		<b>Filing Date</b>	02/5/2002
	Use as many sheets as necessary)		<b>First Named Inventor</b>	Igor KOUZNETSOV et al.
			<b>Group Art Unit</b>	2821
			<b>Examiner Name</b>	Unassigned
		<b>Attorney Docket Number</b>	035905-0118	

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
	B71	ABOU-SAMRA S.J.: "3D CMOS SOI for High Performance Computing", Low Power Electronics and Design Proceedings, 1998.	
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	B74	SCHLAEPPPI H.P.: "Session V: Information Storage Techniques", International Solid-State Circuits Conference, February 11, 1960, pgs. 54-55.	
	B75	DE GRAAF C. et al.: "A Novel High-Density, Low-Cost Diode Programmable Read Only Memory," IEDM, beginning at page 189	
	B76	PETER K. NAJI et al.: "A 256kb 3.0V 1T1MTJ Nonvolatile Magnetoresistive RAM," 2001 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, ISSCC 2001/Session 7/Technology Directions: Advanced Technologies/7.6, February 6, 2001, pp. 122-123 (including enlargement of figures, totaling 9 pages), and associated Visual Supplement, pp. 94-95, 404-405 (enlargements of slides submitted, totaling 25 pages)	
	B77	KIM C. HARDEE et al.: "A Fault-Tolerant 30 ns/375 mW 16K x 1 NMOS Static RAM," IEEE Journal of Solid-State Circuits, October 1981, Vol. SC-16, No. 5, pages 435-443	
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	B80	AKASAKA YOICHI: "Three-dimensional Integrated Circuit: Technology and Application Prospect", Microelectronics Journal, Vol. 20, No.s 1-2, 1989, pgs. 105 - 112.	
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Substitute for form 1449B/PTO		<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Application Number	10/066,376
Date Submitted: June 13, 2002		Filing Date	02/5/2002
Use as many sheets as necessary)		First Named Inventor	Igor KOUZNETSOV et al.
		Group Art Unit	2821
		Examiner Name	Unassigned
		Attorney Docket Number	035905-0118

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Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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	B85	REBER M.: "Benefits of Vertically Stacked Integrated Circuits for Sequential Logic", IEEE, 1996, pgs. 121-124.	
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	B106	Abstract "Technologies Will Pursue Higher DRAM Densities", Electronic News (1991), December 4, 1994, pg. 12.	

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	B109	Abstract "Technique Boosts 3D Memory Density", Electronic Engineering Times, August 29, 1994, pg. 16.	
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	B111	Abstract "MCMs Hit the Road", Electronic Engineering Times, June 15, 1992, pg. 45.	
	B112	Abstract "IEDM Ponders the 'Gigachip' Era", Electronic Engineering Times, January 20, 1992, pg. 33.	
	B113	Abstract "Tech Watch: 1-Gbit DRAM in Sight", Electronic World News, December 16, 1991, pg. 20.	
	B114	Abstract "MCMs Meld into Systems", Electronic Engineering Times, July 22, 1991, pg. 35.	
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	B118	Patent Application, FUJIMOTO et al, US 2002/0027275 A1.	

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> Date Submitted: June 13, 2002 (Use as many sheets as necessary)		Application Number	10/068,376
		Filing Date	02/5/2002
		First Named Inventor	Igor KOUZNETSOV et al.
		Group Art Unit	2821
		Examiner Name	Unassigned
Sheet 7 of 7	Attorney Docket Number	035905-0118	

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	B119	Patent Application, AKRAM, US 2002/0030262 A1.	
	B120	Patent Application, AKRAM, US 2002/0030263 A1.	
	B121	Patent Application, LEEDY, US 2001/0033030 A1.	
	B122	Chan et al. "Three Dimensional CMOS Integrated Circuits on Large Grain Polysilicon Films" IEEE, Hong Kong University of Science and Technology 2000 IEEE	

Examiner  
Signature

Date  
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SUBSTITUTE FOR FORM 14498/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> Date Submitted: September 20, 2002 (use as many sheets as necessary)	1		of	1	<b>Complete If Known</b>	
	Application Number		10/066,376			
	Filing Date		02/05/2002			
	First Named Inventor		Igor G. KOUZNETSOV			
	Group Art Unit		2823			
	Examiner Name		Not Assigned			
Attorney Docket Number		035905-0118				

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	C1	6,110,278		Saxena	8/29/2000	
	C2	RE37,259 E		Ovshinsky	7/3/2001	
	C5	0056838A1		Walker et al	12/27/2001	
	C6	0028541A1		Lee et al	03/07/2002	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

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	C3	"3D Chip-On-Chip Stacking," Semiconductor International, December 1991	
	C4	RICHARD W. LAY: "TRW Develops Wireless Multiboard Interconnect System," Electronic Engineering Times, November 5, 1994	

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Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> Date Submitted: April 5, 2002 (use as many sheets as necessary)		<b>Complete if Known</b>	
Application Number		10/066,376	
Filing Date		February 5, 2002	
First Named Inventor		Igor KOUZNETSOV et al.	
Group Art Unit		2623	
Examiner Name		Unassigned	
Attorney Docket Number		035905-0118	

### U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	A127	6,208,545		Leedy	3/27/2001	

### FOREIGN PATENT DOCUMENTS

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

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